



Master Thesis at Namlab

"Ferroelectric Field Effect Transistors for Next-Generation Ultra-Low Power Memory Cells"

Namlab is offering a Master Thesis concerning the electrical characterization of ferroelectric field effect transistors (FeFET) for novel ultra-low power embedded memories. The work includes device characterization, evaluation of degradation mechanisms, methodology improvement and development of measurement techniques emulating cell array operation of FeFET transistors.

The skills required for this position are:

- Knowledge of semiconductor technology, devices and circuits
- Knowledge of C / LabVIEW / XML would be a plus
- Skill in electrical characterization
- Interest in practical work
- Creativity

Your profile:

- B. Eng. / B. Sc. Degree in Electrical Engineering or Physics
- Knowledge of non-volatile memory concepts preferred
- Organizational skills
- Familiarity with team work

Period:

- Starting date: as soon as possible
- Duration: 6 months

For further information please contact:

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