

Faculty of Electrical and Computer Engineering

Examination office

10.11.2025

Examination plan for

WiSe 25/26

Master's program

Nanoelectronic Systems
3. Semester

only modules for re-
examination!

Examination periode from

09.02. bis 07.03.2026

(subject to further changes)

Examination subject	Date	Professor	Place*	Time Duration
---------------------	------	-----------	--------	------------------

Compulsory modules

Academic and Scientific Work (NES-12 ASW-14.1) M1200-50010 Selection 1 out of 3: Paper Reading Group <i>Mündliche Prüfungsleistung (oral exam)</i>	on appoint.	Prof. Mikolajick	-	-
<i>Präsentation (presentation)</i>	on appoint.		-	-
Photonic Devices Group <i>Beleg (assigned paper)</i>	on appoint.	Prof. Jamshidi	-	-
<i>Präsentation (presentation)</i>	on appoint.		-	-
Nanoelectronic Systems Design Student Conference <i>Beleg (assigned paper)</i>	on appoint.	Prof. Fettweis	-	-
<i>Präsentation (presentation)</i>	on appoint.		-	-
Project Work (NES-12 PW-14.1) M1200-50060 No registration at SELMA required	on appoint.	Prof. Mikolajick	-	-

Optional modules

Innovative Concepts for Active Nanoelectronic Devices (NES-ET-22-E-ICAND) M1212-50063 <i>Klausurarbeit/Mündliche Prüfungsleistung Innovative Semiconductor Devices (oral or written exam)</i> <i>Klausurarbeit/Mündliche Prüfungsleistung Materials for Nanoelectronics (oral or written exam)</i> <i>Protokollsammlung (protocols)</i>	27.02.2026	Prof. Mikolajick		90 min
	19.02.2026	Prof. Richter		90 min
	on appoint.	Prof. Richter	-	-
Communication Networks 3 (NES-12 10 20) M1210-11280 <i>Klausurarbeit/Mündliche Prüfungsleistung (oral or written exam)</i> <i>Projektarbeit (project work)</i>	02.03.2026	Jun.-Prof. Bassoli		120 min
	on appoint.	Jun.-Prof. Bassoli	-	-
Integrated Photonic Devices for Communications and Signal Processing (NES-12 10 06-14.1) M1210-50170 <i>Beleg (assigned paper)</i> <i>Mündliche Prüfungsleistung (oral exam)</i>	on appoint.	Prof. Jamshidi	-	-
	on appoint.	Prof. Jamshidi	-	30 min
Memory Technology (NES-12 12 03-14.1) M1212-1M140	20.02.2026	Prof. Mikolajick	0	90 min

Materials for the 3D System Integration (NES-12 06 01-14.1) M1206-50200 <i>Klausurarbeit Mikro-/Nanowerkstoffsysteme und Zuverlässigkeitsaspekte (written exam)</i> <i>Laborpraktikum Mikro-/Nanowerkstoffsysteme und Zuverlässigkeitsaspekte (lab course)</i>	18.02.2026	Prof. Panchenko		90 min
	on appoint.	Prof. Panchenko	-	-
Electromechanical Networks (NES-12 12 04-14.1) M1212-50130	09.02.2026	Prof. Marschner	0	120 min
Integrated Circuits for Broad-band Optical Communications (NES-12 08 04-14.1) M1208-11230	06.03.2026	Prof. Ellinger	0	120 min
Molecular Electronics (NES-13 14 02-14.1) M1200-50210	on appoint.	Prof. Erbe	-	- 20 min
Optoelectronics (NES-12 12 05-14.1) M1212-50230 <i>Klausurarbeit/Mündliche Prüfungsleistung Optoelectronic Devices and Systems (oral or written exam)</i>	on appoint.	Prof. Lakner	-	- 20 min
<i>Mündliche Prüfungsleistung Nanooptics (oral exam)</i>	on appoint.	Prof. Eng	-	- 20 min
Quantum Mechanics for Nanoelectronics (NES-02 04 01) M1200-50240	on appoint.	Prof. Helm	-	- 30 min
Hardware Modelling and Simulation (NES-11 20 20) M1200-50380	16.02.2026	Prof. Göhringer	0	60 min

Plasma Technology (NES-ET-E-PlaTe) M1211-50480	12.02.2026	Prof. von Hauff	0	90 min
Foundations of Certified Programming Language and Compiler Design (NES-INF-E-FCPL) M1200-50460	on appoint.	Dr. Ertel	-	- 30 min
Computational Laser Systems (NES-ET-E-ComLS-23) M1208-50490	on appoint.	Prof. Czarske	-	- 30 min

Re-examination

Radio Frequency Integrated Circuits (NES-12 08 02-14.1) M1208-11100	11.02.2026	Prof. Ellinger Dr. Schumann	0	120 min
Hardware/Software Codesign (NES-12 10 03-14.3) M1210-50030	24.02.2026	Prof. Fettweis Dr. Matus	0	120 min
Semiconductor Technology (NES-12 12 02-19.1) M1212-50080	04.03.2026	Prof. Mannsfeld Dr. Hambsch	0	120 min
Communications (NES-12 10 02-14.1) M1210-50100	13.02.2026	Prof. Fettweis	0	120 min
Neuromorphic VLSI Systems (NES-12 08 06) M1208-11360 <i>Beleg (assigned paper)</i>	on appoint.	Prof. Mayr	-	-
Materials for the 3D System Integration (NES-12 06 01-14.1) M1206-50200 <i>Klausurarbeit 3D-Systeminte- gration and 3D Technologies (written exam)</i>	on appoint.	Prof. Panchenko	-	-

VLSI Processor Design (NES-12 08 7) M1208-50320 <i>Projektarbeit (project work)</i>	on appoint.	Prof. Mayr	-	-
<i>Referat (presentation)</i>	on appoint.	Prof. Mayr	-	-

*** Floor plan: see notice board!**

signed Prof. Dr.-Ing. T. Mikolajick
Chairman of the Examination Board