Timetable 1st semester (winter term 2025/26)

Time/Day	Monday	Tuesday	Wednesday	Thursday	Friday
1 DS **7:30 a.m. -** **9:00 a.m.**				**E: Confidential Computing** Fetzer *INF-NES-C-CONF* *APB/E023/U*	
2 DS **9:20 a.m. -** **10:50 a.m.**	**L: Quantum and Solid State Physics** Dianat *PHY-NES-E-QMNE Quantum Mechanics for Nanoelectronics* *VMB/0302/U*	**L: Neural Networks and Memristive Hardware Accelerators** Seitz/ Schroedter *EuI-NES-E-NNMHA* *TOE/0317/H* **L: Integrated Photonic Devices** Jamshidi *EuI-NES-E-IPD Integrated Photonic Devices for Communications and Signal Processing* *BAR/0I89/U* **L: Foundations of Certified Programming Language and Compiler Design** Ertel *EuI-NES-E-FCPL* *APB/E001/U* **L: IoT Communication** Dargie/ Wählisch *INF-NES-E-IoT* *APB/E023/U*	**L: Confidential Computing** Fetzer *INF-NES-C-CONF* *BEY/0138/H*	**L: Semiconductor Technology 1** Mannsfeld *EuI-NES-C-SCT Semiconductor Technology* *TOE/0317/H*	**P: Python for Engineers** Knoll EuI-NES-E-NNMHA Neural Networks and Memristive Hardware Accelerators *TOE/0317/H*
3 DS **11:10 a.m. -** **12:40 p.m.**	**L: Systems Engineering 1** Fetzer *INF-NES-E-SE1 Foundations of Systems Engineering* *APB/E023/U*	**L: Stochastic Signals and Systems** Kortke *EuI-NES-E-StSig* *GÖR/0229/U*		**L: Quantum and Solid State Physics** Dianat *PHY-NES-E-QMNE Quantum Mechanics for Nanoelectronics* *ZEU/0147/Z* **2nd week!** **E: Quantum and Solid State Physics** Dianat *PHY-NES-E-QMNE Quantum Mechanics for Nanoelectronics* *ZEU/0147/Z* **1st week!**	**E: Systems Engineering 1** Fetzer *INF-NES-E-SE1 Foundations of Systems Engineering* *APB/E023/U*
4 DS **01:00 p.m. -** **02:30 p.m.**		**L: Semiconductor Technology 1** Mannsfeld *EuI-NES-C-SCT Semiconductor Technology* *TOE/0317/H*	**E: Stochastic Signals and Systems** Kortke *EuI-NES-E-StSig* *GÖR/0229/U*	**L: Plasma Technology** Hauff *EuI-NES-E-PlaTe* *BAR/0E85/U* **L: Distributed Systems** Springer *INF-NES-E-DS* *APB/E023/U* **E: Foundations of Certified Programming Language and Compiler Design** Ertel/ Castrillon-Mazo *EuI-NES-E-FCPL* *APB/E006/U*	**P: RoboLab** Knobloch *INF-NES-C-LabS Lab Sessions* *HÜL/S186/H*
5 DS **02:50 p.m. -** **04:20 p.m.**		**L: Semiconductor Quantum Structures** Dimakis/Helm/Winnerl/Erbe *PHY-NES-E-QMNE Quantum Mechanics for Nanoelectronics* *REC/B214/H*	**P: Integrated Photonic Devices** Jamshidi *EuI-NES-E-IPD Integrated Photonic Devices for Communications and Signal Processing* *BAR/0213/H*	**L: Plasma Technology** Hauff *EuI-NES-E-PlaTe* *BAR/0E85/U* **L: Integrated Photonic Devices** Jamshidi *EuI-NES-E-IPD Integrated Photonic Devices for Communications and Signal processing* *BAR/0I89/U* **E: Distributed Systems** Springer *INF-NES-E-DS* *APB/E023/U*	**E: Plasma Technology** Hauff *EuI-NES-E-PlaTe* *BAR/0E85/U*
6 DS **04:40 p.m. -** **06:10 p.m.**	**L: Hardware Modelling and Simulation** Göhringer *INF-NES-E-HM* *SCH/A215/H*	**E: IoT Communication** Dargie/ Wählisch/ Pang *INF-NES-E-IoT* *APB/E005/U*			

L = Lecture
E = Exercise
P = Practical Lab Course
DS = Double Period
1st week = odd week
2nd week = even week

German Language Courses:
Please register in OPAL for one course:
https://bildungsportal.sachsen.de/opal/auth/RepositoryEntry/49604263939?11
Registration starts on 1st of October, courses start on 20.10. - 24.10.2025.

If you wish to attend a higher level, please do a placement test at the beginning of the semester. Read here:
https://www.sprachausbildung.tu-dresden.de/en/enrolment/placement-tests/#1658737554952-3b9ae164-7e33

2nd of September, 2025, subject to changes

Mandatory courses in red lettering!
Same module parts in same background colour

Modules currently in the 3rd semester timetable that can also be taken in 1st semester in blue lettering
PHY-NES-E-QMNE Quantum Mechanics for Nanoelectronics
EuI-NES-E-IPD Integrated Photonic Devices for Communications and Signal Processing
INF-NES-E-IoT IoT Communication
INF-NES-E-DS Distributed Systems
INF-NES-E-HMS Hardware Modelling and Simulation
EuI-NES-E-FCPL Foundations of Certified Programming Language and Compiler Design