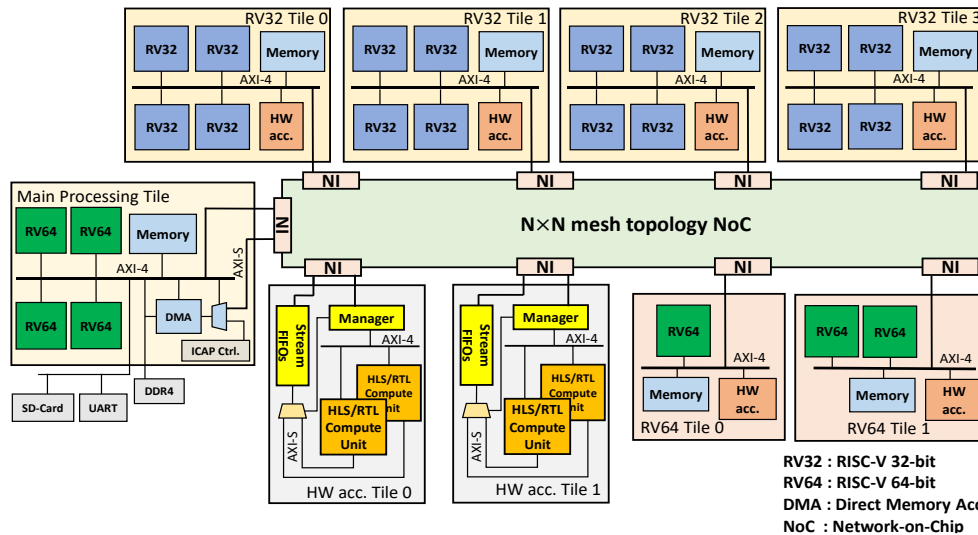


Machine Learning Acceleration on MARIE Platform

Potential tasks:

- Accelerating linear algebra functions for matrix operations (e.g., GEMM, Inverse, mldivide)
- Developing of software functions for a set of ML kernels running on MARIE MPSoC*
- Extending MARIE MPSoC with custom hardware accelerators for ML and linear algebra functions



MARIE Platform

RV32 : RISC-V 32-bit
RV64 : RISC-V 64-bit
DMA : Direct Memory Access
NoC : Network-on-Chip

*MPSoC: Multiprocessor System-on-Chip

Contact Person:

Ahmed Kamaleldin

Required programming skills: C/C++, HDL

(VHDL or Verilog)

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